

DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] It is the sectional view of the chip resistor obtained according to the example of this invention.

[Drawing 2] It is process drawing for explaining the example of this invention.

[Drawing 3] It is the sectional view of the chip resistor obtained according to other examples of this invention.

[Description of Notations]

1 Insulating Substrate

2 Resistor Layer

3 Label Mark Patterned Layer

3a Protection patterned layer

4 Resin Protective Layer

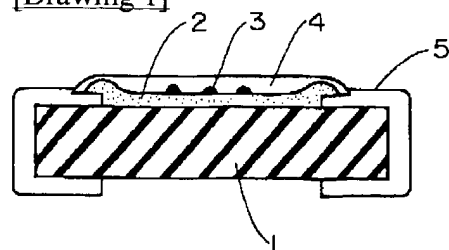
5 Electrode Layer

6 Photopolymer Layer

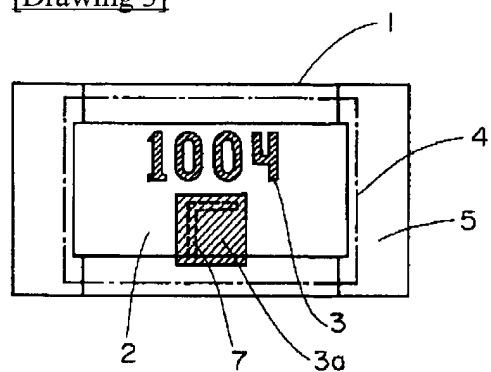
7 Trimming Slot

DRAWINGS

[Drawing 1]

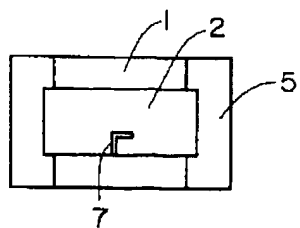


[Drawing 3]

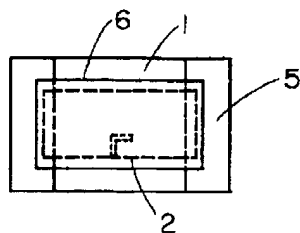


[Drawing 2]

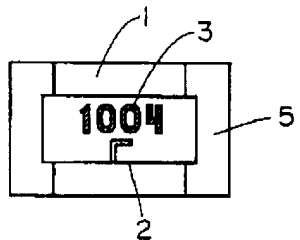
(a)



(b)



(c)



(d)

